

S.NO: 22N1- UCS

Course Code: BXJ

A.D.M.COLLEGE FOR WOMEN, NAGAPATTINAM

(AUTONOMOUS)

B.Sc (Computer Science) Degree Examination

V Semester – November – 2022

CC X – COMPUTER ORGANIZATION AND ARCHITECTURE

Time: 3 hours

Maximum Marks: 75

Section –A

10X2=20

Answer **ALL** the Questions

1. What is Instruction Code?
 2. Define Instruction Format.
 3. Define Register Stack.
 4. What is reverse Polish Notation?
 5. Write about Array Multiplier.
 6. What do you know about restoring method?
 7. Define Baud rate.
 8. Write about interrupt vector.
 9. Describe the principle of Multiprogramming.
 10. Illustrate Bootstrap loader.
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Section -B

5X5=25

Answer **ALL** the Questions

11. a) Draw the flowchart for interrupt cycle. Explain it.

(or)

b) Explain in detail the different instruction formats with an example.

12. a) Categorize the various types of CPU organization.

(or)

b) Write a note on: i) Implied Addressing Mode ii) Register Indirect Mode

13. a) Outline the Multiplication Algorithm.

(or)

b) Explain about Divide Overflow in detail.

14. a) List out mode of transfer in detail.

(or)

b) Write a note on: i) Strobe Control ii) Handshaking

15. a) Explain in detail about Virtual Memory.

(or)

b) Outline Memory Protection in detail.

Section -C

3 X 10 = 30

Answer any **THREE** Questions

16. Explain about Instruction Cycle in detail.
17. How do you evaluate Arithmetic Expression using stack? Explain it.
18. Explain in detail about Addition and Subtraction of Floating Point numbers.
19. Explain DMA in detail.
20. Explain Mapping and the various types of Mapping.

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